

PATENT AND TRADEMARK OFFICE

APPLICANTS:

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APPLICATION NO.:

10/669,119 (REI OF 5,996,036)

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TITLE:

BUS TRANSACTION REORDERING IN A COMPUTER SYSTEM HAVING

UNORDERED SLAVES

EXAMINER:

Gopal C. Ray

GROUP ART UNIT:

2111

ATTY. DKT. NO.:

18602-08098 (P2080R1C1)

CERTIFICATE OF MAILING				
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AMENDMENT A

IN THE ABSTRACT

Replace the Abstract with the following:

[[A mechanism is provided for reordering]] Reordering bus transactions [[to increase]] increases bus utilization [[in a computer system in which]] where a split-transaction bus is bridged to a single-envelope bus. In one embodiment, both masters and slaves are ordered[[, simplifying implementation. In]]; in another embodiment, [[the system is more loosely coupled with]] only masters [[being]] are ordered. Greater bus utilization is thereby achieved. To avoid

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